Single Phase Fully Controlled Rectifier
Operation and Analysis of single phase fully controlled converter.

**Instructional Objectives**

On completion the student will be able to

- Differentiate between the constructional and operation features of uncontrolled and controlled converters
- Draw the waveforms and calculate their average and RMS values of different variables associated with a single phase fully controlled half wave converter.
- Explain the operating principle of a single phase fully controlled bridge converter.
- Identify the mode of operation of the converter (continuous or discontinuous) for a given load parameters and firing angle.
- Analyze the converter operation in both continuous and discontinuous conduction mode and thereby find out the average and RMS values of input/output, voltage/currents.
- Explain the operation of the converter in the inverter mode.
10.1 Introduction

Single phase uncontrolled rectifiers are extensively used in a number of power electronic based converters. In most cases they are used to provide an intermediate unregulated dc voltage source which is further processed to obtain a regulated dc or ac output. They have, in general, been proved to be efficient and robust power stages. However, they suffer from a few disadvantages. The main among them is their inability to control the output dc voltage / current magnitude when the input ac voltage and load parameters remain fixed. They are also unidirectional in the sense that they allow electrical power to flow from the ac side to the dc side only. These two disadvantages are the direct consequences of using power diodes in these converters which can block voltage only in one direction. As will be shown in this module, these two disadvantages are overcome if the diodes are replaced by thyristors, the resulting converters are called fully controlled converters.

Thyristors are semiconducted devices which can be turned ON by applying a current pulse at its gate terminal at a desired instance. However, they cannot be turned off from the gate terminals. Therefore, the fully controlled converter continues to exhibit load dependent output voltage / current waveforms as in the case of their uncontrolled counterpart. However, since the thyristor can block forward voltage, the output voltage / current magnitude can be controlled by controlling the turn on instants of the thyristors. Working principle of thyristors based single phase fully controlled rectifiers will be explained first in the case of a single thyristor halfwave rectifier circuit supplying an R or R-L load. However, such converters are rarely used in practice.

Full bridge is the most popular configuration used with single phase fully controlled rectifiers. Analysis and performance of this rectifier supplying an R-L-E load (which may represent a dc motor) will be studied in detail in this lesson.
10.2 Single phase fully controlled halfwave rectifier

10.2.1 Resistive load

Fig. 10.1(a) shows the circuit diagram of a single phase fully controlled halfwave rectifier supplying a purely resistive load. At $\omega t = 0$ when the input supply voltage becomes positive the thyristor T becomes forward biased. However, unlike a diode, it does not turn ON till a gate pulse is applied at $\omega t = \alpha$. During the period $0 < \omega t \leq \alpha$, the thyristor blocks the supply voltage and the load voltage remains zero as shown in fig 10.1(b). Consequently, no load current flows during this interval. As soon as a gate pulse is applied to the thyristor at $\omega t = \alpha$ it turns ON. The voltage across the thyristor collapses to almost zero and the full supply voltage appears across the load. From this point onwards the load voltage follows the supply voltage. The load being purely resistive the load current $i_o$ is proportional to the load voltage. At $\omega t = \pi$ as the supply voltage passes through the negative going zero crossing the load voltage and hence the load current becomes zero and tries to reverse direction. In the process the thyristor undergoes reverse recovery and starts blocking the negative supply voltage. Therefore, the load voltage and the load current remains clamped at zero till the thyristor is fired again at $\omega t = 2\pi + \alpha$. The same process repeats there after.

From the discussion above and Fig 10.1 (b) one can write

For $\alpha < \omega t \leq \pi$

$$v_o = v_i = \sqrt{2} V_i \sin \omega t \quad (10.1)$$

$$i_o = \frac{v_o}{R} = \sqrt{2} \frac{V_i}{R} \sin \omega t \quad (10.2)$$
\[ v_0 = i_0 = 0 \quad \text{otherwise.} \]

Therefore
\[ V_{OAV} = \frac{1}{2\pi} \int_0^{2\pi} v_o \, dv_o = \frac{1}{2\pi} \int_0^{\pi} \sqrt{2} \, V_i \, \sin \omega t \, d\omega t \]

Or
\[ V_{OAV} = \frac{V_i}{\sqrt{2\pi}} \]

\[ V_{ORMS} = \sqrt{\frac{1}{2\pi} \int_0^{2\pi} v_o^2 \, dv_o} \]
\[ = \sqrt{\frac{1}{2\pi} \int_0^{\pi} 2V_i^2 \sin^2 \omega t \, d\omega t} \]
\[ = \frac{V_i^2}{\sqrt{2\pi}} \int_0^{\pi} (1 - \cos 2\omega t) \, d\omega t \]
\[ = \frac{V_i^2}{\sqrt{2\pi}} \left[ \pi - \alpha + \frac{\sin 2\alpha}{2} \right] \]
\[ = \frac{V_i}{\sqrt{2}} \left( 1 - \frac{\alpha}{\pi} + \frac{\sin 2\alpha}{2\pi} \right)^{\frac{1}{2}} \]

\[ \therefore \quad \frac{V_{ORMS}}{V_{OAV}} = \frac{\pi \left( 1 - \frac{\alpha}{\pi} + \frac{\sin 2\alpha}{2\pi} \right)^{\frac{1}{2}}}{\left( 1 + \cos \alpha \right)} \]

Similar calculation can be done for \( i_0 \). In particulars for pure resistive loads \( FF_{io} = FF_{vo} \).

### 10.2.2 Resistive-Inductive load

Fig 10.2 (a) and (b) shows the circuit diagram and the waveforms of a single phase fully controlled halfwave rectifier supplying a resistive inductive load. Although this circuit is hardly used in practice its analysis does provide useful insight into the operation of fully controlled rectifiers which will help to appreciate the operation of single phase bridge converters to be discussed later.
As in the case of a resistive load, the thyristor T becomes forward biased when the supply voltage becomes positive at $\omega t = 0$. However, it does not start conduction until a gate pulse is applied at $\omega t = \alpha$. As the thyristor turns ON at $\omega t = \alpha$ the input voltage appears across the load and the load current starts building up. However, unlike a resistive load, the load current does not become zero at $\omega t = \pi$, instead it continues to flow through the thyristor and the negative supply voltage appears across the load forcing the load current to decrease. Finally, at $\omega t = \beta$ ($\beta > \pi$) the load current becomes zero and the thyristor undergoes reverse recovery. From this point onwards the thyristor starts blocking the supply voltage and the load voltage remains zero until the thyristor is turned ON again in the next cycle. It is to be noted that the value of $\beta$ depends on the load parameters. Therefore, unlike the resistive load the average and RMS output voltage depends on the load parameters. Since the thyristors does not conduct over the entire input supply cycle this mode of operation is called the “discontinuous conduction mode”.

From above discussion one can write.

For $\alpha \leq \omega t \leq \beta$

$$v_0 = v_i = \sqrt{2} V_i \sin \omega t$$

$v_0 = 0$ otherwise

Therefore

$$V_{OAV} = \frac{1}{2\pi} \int_0^{2\pi} v_0 \, \dot{v} \, dt$$

$$V_{OAV} = \frac{1}{2\pi} \int_{\alpha}^{\beta} \sqrt{2} V_i \sin \omega t \, \dot{v} \, dt$$

\[\text{Fig. 10.2: Single phase fully controlled half wave rectifier supplying a resistive inductive load}\]
\[
V = \frac{V_i}{\sqrt{2\pi}}(\cos\alpha - \cos\beta)
\]

\[
V_{\text{ORMS}} = \frac{1}{\sqrt{2\pi}} \int_0^{2\pi} v_0^2 \, \text{d}\omega
\]

\[
= \frac{1}{\sqrt{2\pi}} \int_\alpha^\beta 2v_i^2 \sin^2 \omega \, \text{d}\omega
\]

\[
= \frac{V_i}{\sqrt{2}} \left( \frac{\beta - \alpha}{\pi} + \frac{\sin 2\alpha - \sin 2\beta}{2\pi} \right)^\frac{1}{2}
\]

\[
I_{\text{OAV}} = \frac{V_{\text{OAV}}}{R} = \frac{V_i}{\sqrt{2\pi R}} (\cos\alpha - \cos\beta)
\]

(10.9)

(10.10)

Since the average voltage drop across the inductor is zero.

However, \( I_{\text{ORMS}} \) cannot be obtained from \( V_{\text{ORMS}} \) directly. For that a closed from expression for \( i_0 \) will be required. The value of \( \beta \) in terms of the circuit parameters can also be found from the expression of \( i_0 \).

For \( \alpha \leq \omega t \leq \beta \)

\[
Ri_o + L \frac{di_o}{dt} = v_o = \sqrt{2} V_i \sin \omega t
\]

(10.11)

The general solution of which is given by

\[
i_o = I_o e^{\frac{(\omega t - \alpha)}{\tan \phi}} + \frac{\sqrt{2} V_i}{Z} \sin(\omega t - \varphi)
\]

(10.12)

Where \( \tan \varphi = \frac{\omega L}{R} \) and \( Z = \sqrt{R^2 + \omega^2 L^2} \)

\[
i_o \bigg|_{\omega t = \alpha} = 0
\]

\[
\therefore 0 = I_o + \frac{\sqrt{2} V_i}{Z} \sin(\alpha - \varphi)
\]

\[
i_o = \frac{\sqrt{2} V_i}{Z} \left[ \sin(\varphi - \alpha) e^{\frac{(\omega t - \alpha)}{\tan \phi}} + \sin(\omega t - \varphi) \right]
\]

(10.13)

\[
i_o = 0 \text{ otherwise.}
\]

Equation (10.13) can be used to find out \( I_{\text{ORMS}} \). To find out \( \beta \) it is noted that

\[
i_o \bigg|_{\omega t = \beta} = 0
\]

\[
\therefore \sin(\varphi - \alpha) e^{\frac{\alpha - \beta}{\tan \phi}} = \sin(\varphi - \beta)
\]

(10.14)

Equation (10.14) can be solved to find out \( \beta \)

**Exercise 10.1**

Fill in the blank(s) with appropriate word(s)
i) In a single phase fully controlled converter the _________ of an uncontrolled converters are replaced by ____________.

ii) In a fully controlled converter the load voltage is controlled by controlling the _________ of the converter.

iii) A single phase half wave controlled converter always operates in the _________ conduction mode.

iv) The voltage form factor of a single phase fully controlled half wave converter with a resistive inductive load is ________ compared to the same converter with a resistive load.

v) The load current form factor of a single phase fully controlled half wave converter with a resistive inductive load is ________ compared to the same converter with a resistive load.

Answers: (i) diodes, thyristors; (ii) firing angle; (iii) discontinuous (iv) poorer; (v) better.

2) Explain qualitatively, what will happen if a free-wheeling diode(cathode of the diode shorted with the cathode of the thyristor) is connected across the load in Fig 10.2.(a)

Answer: Referring to Fig 10.2(b), the free wheeling diode will remain off till \( \omega t = \pi \) since the positive load voltage across the load will reverse bias the diode. However, beyond this point as the load voltage tends to become negative the free wheeling diode comes into conduction. The load voltage is clamped to zero there after. As a result

i) Average load voltage increases

ii) RMS load voltage reduces and hence the load voltage form factor reduces.

iii) Conduction angle of load current increases as does its average value. The load current ripple factor reduces.
10.3 Single phase fully controlled bridge converter

Fig 10.3 (a) shows the circuit diagram of a single phase fully controlled bridge converter. It is one of the most popular converter circuits and is widely used in the speed control of separately excited dc machines. Indeed, the R–L–E load shown in this figure may represent the electrical equivalent circuit of a separately excited dc motor.

The single phase fully controlled bridge converter is obtained by replacing all the diode of the corresponding uncontrolled converter by thyristors. Thyristors $T_1$ and $T_2$ are fired together while $T_3$ and $T_4$ are fired 180° after $T_1$ and $T_2$. From the circuit diagram of Fig 10.3(a) it is clear that for any load current to flow at least one thyristor from the top group ($T_1$, $T_3$) and one thyristor from the bottom group ($T_2$, $T_4$) must conduct. It can also be argued that neither $T_1T_3$ nor $T_2T_4$ can conduct simultaneously. For example whenever $T_3$ and $T_4$ are in the forward blocking state and a gate pulse is applied to them, they turn ON and at the same time a negative voltage is applied across $T_1$ and $T_2$ commutating them immediately. Similar argument holds for $T_1$ and $T_2$.

For the same reason $T_1T_4$ or $T_2T_3$ can not conduct simultaneously. Therefore, the only possible conduction modes when the current $i_0$ can flow are $T_1T_2$ and $T_3T_4$. Of course it is possible that at a given moment none of the thyristors conduct. This situation will typically occur when the load current becomes zero in between the firings of $T_1T_2$ and $T_3T_4$. Once the load current becomes zero all thyristors remain off. In this mode the load current remains zero. Consequently the converter is said to be operating in the discontinuous conduction mode.

Fig 10.3 (b) shows the voltage across different devices and the dc output voltage during each of these conduction modes. It is to be noted that whenever $T_1$ and $T_2$ conducts, the voltage across $T_3$ and $T_4$ becomes $-v_i$. Therefore $T_3$ and $T_4$ can be fired only when $v_i$ is negative i.e, over the negative half cycle of the input supply voltage. Similarly $T_1$ and $T_2$ can be fired only over the positive half cycle of the input supply. The voltage across the devices when none of the thyristors conduct depends on the off state impedance of each device. The values listed in Fig 10.3 (b) assume identical devices.

Under normal operating condition of the converter the load current may or may not remain zero over some interval of the input voltage cycle. If $i_0$ is always greater than zero then the converter is said to be operating in the continuous conduction mode. In this mode of operation of the converter $T_1T_2$ and $T_3T_4$ conducts for alternate half cycle of the input supply.
However, in the discontinuous conduction mode none of the thyristors conduct over some portion of the input cycle. The load current remains zero during that period.

### 10.3.1 Operation in the continuous conduction mode

As has been explained earlier in the continuous conduction mode of operation $i_0$ never becomes zero, therefore, either $T_1T_2$ or $T_3T_4$ conducts. Fig 10.4 shows the waveforms of different variables in the steady state. The firing angle of the converter is $\alpha$. The angle $\theta$ is given by

$$\sin\theta = \frac{E}{\sqrt{2}V_1}$$  \hspace{1cm} (10.15)

It is assumed that at $t = 0$ $T_3T_4$ was conducting. As $T_1T_2$ are fired at $\omega t = \alpha$ they turn on commutating $T_3T_4$ immediately. $T_3T_4$ are again fired at $\omega t = \pi + \alpha$. Till this point $T_1T_2$ conducts. The period of conduction of different thyristors are pictorially depicted in the second waveform (also called the conduction diagram) of Fig 10.4.
Fig. 10.4: Waveforms in Single phase fully controlled bridge convertor in continuous conduction mode.
The dc link voltage waveform shown next follows from this conduction diagram and the conduction table shown in Fig 10.3(b). It is observed that the emf source E is greater than the dc link voltage till $\omega t = \alpha$. Therefore, the load current $i_0$ continues to fall till this point. However, as $T_1T_2$ are fired at this point $v_0$ becomes greater than E and $i_0$ starts increasing through R-L and E. At $\omega t = \pi - \theta$ $v_0$ again equals E. Depending upon the load circuit parameters $i_0$ reaches its maximum at around this point and starts falling afterwards. Continuous conduction mode will be possible only if $i_0$ remains greater than zero till $T_3T_4$ are fired at $\omega t = \pi + \alpha$ where upon the same process repeats. The resulting $i_0$ waveform is shown below $v_0$. The input ac current waveform $i_i$ is obtained from $i_0$ by noting that whenever $T_1T_2$ conducts $i_i = i_0$ and $i_i = -i_0$ whenever $T_3T_4$ conducts. The last waveform shows the typical voltage waveform across the thyristor $T_1$. It is to be noted that when the thyristor turns off at $\omega t = \pi + \alpha$ a negative voltage is applied across it for a duration of $\pi - \alpha$. The thyristor must turn off during this interval for successful operation of the converter.

It is noted that the dc voltage waveform is periodic over half the input cycle. Therefore, it can be expressed in a Fourier series as follows.

$$v_0 = V_{OAV} + \sum_{n=1}^{a} [v_{an} \cos(2n\omega t) + v_{bn} \sin(2n\omega t)]$$  \hspace{1cm} (10.16)

Where

$$V_{OAV} = \frac{1}{\pi} \int_{a}^{\pi+a} v_0 \, \text{d}t = \frac{2\sqrt{2}}{\pi} V_i \cos \alpha$$ \hspace{1cm} (10.17)

$$v_{an} = \frac{2}{\pi} \int_{0}^{\pi} v_0 \cos(2n\omega t) \, \text{d}t = \frac{2\sqrt{2}}{\pi} V_i \left[ \frac{\cos((2n+1)\alpha) - \cos((2n-1)\alpha)}{2n+1 - 2n-1} \right]$$ \hspace{1cm} (10.18)

$$v_{bn} = \frac{2}{\pi} \int_{0}^{\pi} v_0 \sin(2n\omega t) \, \text{d}t = \frac{2\sqrt{2}}{\pi} V_i \left[ \frac{\sin((2n+1)\alpha) - \sin((2n-1)\alpha)}{2n+1 - 2n-1} \right]$$ \hspace{1cm} (10.19)

Therefore the RMS value of the nth harmonic

$$V_{onRMS} = \frac{1}{\sqrt{2}} \sqrt{v_{an}^2 + v_{bn}^2}$$ \hspace{1cm} (10.20)

RMS value of $v_0$ can of course be completed directly from.

$$V_{ORMS} = \sqrt{\frac{1}{\pi} \int_{a}^{\pi+a} v_0^2 \, \text{d}t} = V_i$$ \hspace{1cm} (10.21)

Fourier series expression of $v_0$ is important because it provides a simple method of estimating individual and total RMS harmonic current injected into the load as follows:

The impedance offered by the load at nth harmonic frequency is given by

$$Z_n = \sqrt{R^2 + (2n\omega L)^2}$$ \hspace{1cm} (10.22)

$$I_{onRMS} = \frac{V_{onRMS}}{Z_n}; \quad I_{OHRMS} = \left[ \sum_{n=1}^{a} I_{onRMS}^2 \right]^{1/2}$$ \hspace{1cm} (10.23)

From (10.18) – (10.23) it can be argued that in an inductive circuit $I_{onRMS} \rightarrow 0$ as fast as $1/n^2$. So in practice it will be sufficient to consider only first few harmonics to obtain a reasonably accurate estimate of $I_{OHRMS}$ form equation 10.23. This method will be useful, for example, while calculating the required current derating of a dc motor to be used with such a converter.

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However to obtain the current rating of the device to be used it is necessary to find out a closed form expression of \( i_0 \). This will also help to establish the condition under which the converter will operate in the continuous conduction mode.

To begin with we observe that the voltage waveform and hence the current waveform is periodic over an interval \( \pi \). Therefore, finding out an expression for \( i_0 \) over any interval of length \( \pi \) will be sufficient. We choose the interval \( \alpha \leq \omega t \leq \pi + \alpha \).

In this interval
\[
L \frac{di_0}{dt} + Ri_0 + E = \sqrt{2}V_i \sin \omega t
\]
(10.24)
The general solution of which is given by
\[
i_0 = I e^{\frac{(\omega t - \alpha)}{\tan \varphi}} + \frac{\sqrt{2}V_i}{Z} \left[ \sin(\omega t - \varphi) - \frac{\sin \theta}{\cos \varphi} \right]
\]
(10.25)
Where,
\[
Z = \sqrt{R^2 + \omega^2 L^2}; \quad \tan \varphi = \frac{\omega L}{R}; \quad E = \sqrt{2}V_i \sin \theta; \quad R = Z \cos \varphi
\]
Now at steady state \( i_0 \big|_{\omega t = \alpha} = i_0 \big|_{\omega t = \pi + \alpha} \) since \( i_0 \) is periodic over the chosen interval. Using this boundary condition we obtain
\[
i_0 = \frac{\sqrt{2}V_i}{Z} \left[ \frac{2 \sin(\varphi - \alpha)}{\tan \varphi} \left\{ e^{\frac{(\omega t - \alpha)}{\tan \varphi}} + \sin(\omega t - \varphi) \cdot \frac{\sin \theta}{\cos \varphi} \right\} \right]
\]
(10.26)
The input current \( i_i \) is related to \( i_0 \) as follows:
\[
i_i = i_0 \quad \text{for} \quad \alpha \leq \omega t \leq \pi + \alpha
\]
\[
i_i = -i_0 \quad \text{otherwise.}
\]
(10.27)
Fig 10.5 shows the waveform of \( i_i \) in relation to the \( v_i \) waveform.
It will be of interest to find out a Fourier series expression of $i_i$. However, using actual expression for $i_i$ will lead to exceedingly complex calculation. Significant simplification can be made by replacing $i_0$ with its average value $I_0$. This will be justified provided the load is highly inductive and the ripple on $i_0$ is negligible compared to $I_0$. Under this assumption the idealized waveform of $i_i$ becomes a square wave with transitions at $\omega t = \alpha$ and $\omega t = \alpha + \pi$ as shown in Fig 10.5. $i_{i1}$ is the fundamental component of this idealized $i_i$.

Evidently the input current displacement factor defined as the cosine of the angle between input voltage ($v_i$) and the fundamental component of input current ($i_{i1}$) waveforms is $\cos \alpha$ (lagging).

It can be shown that

$$I_{i1\text{RMS}} = \frac{2\sqrt{2}}{\pi} I_0$$

(10.28)
and \[ I_{\text{RMS}} = I_0 \] (10.29)

Therefore the input current distortion factor \[ \frac{I_{\text{HRMS}}}{I_{\text{RMS}}} = \frac{2\sqrt{2}}{\pi} \] (10.30)

The input power factor \[ = \frac{\text{Actual Power}}{\text{Apparent Power}} = \frac{V_{i}I_{\text{HRMS}} \cos \alpha}{V_{i}I_{\text{RMS}}} \]

\[ = \frac{2\sqrt{2}}{\pi} \cos \alpha \quad \text{(lagging)} \] (10.31)

Therefore, the rectifier appears as a lagging power factor load to the input ac system. Larger the ‘\( \alpha \)’ poorer is the power factor.

The input current \( i_i \) also contain significant amount of harmonic current (3rd, 5th, etc) and therefore appears as a harmonic source to the utility. Exact composition of the harmonic currents can be obtained by Fourier series analysis of \( i_i \) and is left as an exercise.

**Exercise 10.2**

Fill in the blank(s) with the appropriate word(s).

i) A single phase fully controlled bridge converter can operate either in the _________ or _________ conduction mode.

ii) In the continuous conduction mode at least _________ thyristors conduct at all times.

iii) In the continuous conduction mode the output voltage waveform does not depend on the _________ parameters.

iv) The minimum frequency of the output voltage harmonic in a single phase fully controlled bridge converter is _________ the input supply frequency.

v) The input displacement factor of a single phase fully controlled bridge converter in the continuous conduction mode is equal to the cosine of the _________ angle.

**Answer:**
(i) continuous, discontinuous; (ii) two; (iii) load; (iv) twice; (v) firing.

2. A single phase fully controlled bridge converter operates in the continuous conduction mode from a 230V, 50HZ single phase supply with a firing angle \( \alpha = 30^\circ \). The load resistance and inductances are 10Ω and 50mH respectively. Find out the 6th harmonic load current as a percentage of the average load current.

**Answer:**

The average dc output voltage is

\[ V_{OAV} = \frac{2\sqrt{2}}{\pi}V_{i} \cos \alpha = 179.33 \text{ Volts} \]

Average output load current \[ = \frac{V_{OAV}}{R_L} = 17.93 \text{ Amps} \]

From equation (10.18) \( V_{h3} = 10.25 \text{ Volts} \)

From equation (10.19) \( V_{h3} = 35.5 \text{ Volts} \)

\[ V_{03\text{RMS}} = 26.126 \text{ Volts}, \quad Z_3 = \sqrt{R_L^2 + (6 \times 2 \times \pi \times 50 \times 50 \times 10^{-3})^2} = 94.78 \text{ ohms} \]
\[ I_{3RMS} = \frac{V_{0RMS}}{Z_3} = 0.2756 \text{ Amps} = 1.54\% \text{ of } I_{OAV}. \]

### 10.3.2 Operation in the discontinuous conduction mode

So far we have assumed that the converter operates in continuous conduction mode without paying attention to the load condition required for it. In figure 10.4 the voltage across the R and L component of the load is negative in the region \( \pi - \theta \leq \omega t \leq \pi + \alpha \). Therefore \( i_0 \) continues to decrease till a new pair of thyristor is fired at \( \omega t = \pi + \alpha \). Now if the value of R, L and E are such that \( i_0 \) becomes zero before \( \omega t = \pi + \alpha \) the conduction becomes discontinuous. Obviously then, at the boundary between continuous and discontinuous conduction the minimum value of \( i_0 \) which occurs at \( \omega t = \alpha \) and \( \omega t = \pi + \alpha \) will be zero. Putting this condition in (10.26) we obtain the condition for continuous conduction as.

\[
\frac{2\sin(\phi - \alpha)}{1 - e^{-\frac{\pi}{\tan \phi}}} - \frac{\sin(\phi - \alpha)}{\cos \phi} \geq 0 \quad \text{(10.32)}
\]
Fig 10.6 shows waveforms of different variables on the boundary between continuous and discontinuous conduction modes and in the discontinuous conduction mode. It should be stressed that on the boundary between continuous and discontinuous conduction modes the load current is still continuous. Therefore, all the analysis of continuous conduction mode applies to this case as well. However in the discontinuous conduction mode $i_0$ remains zero for certain interval. During this interval none of the thyristors conduct. These intervals are shown by hatched lines in the conduction diagram of Fig 10.6(b). In this conduction mode $i_0$ starts rising from zero as $T_1T_2$ are fired at $\omega t = \alpha$. The load current continues to increase till $\omega t = \pi - \theta$. After this, the output voltage $v_0$ falls below the emf $E$ and $i_0$ decreases till $\omega t = \beta$ when it becomes zero. Since the thyristors cannot conduct current in the reverse direction $i_0$ remains at zero till $\omega t = \pi + \alpha$ when $T_3$ and $T_4$ are fired. During the period $\beta \leq \omega t \leq \pi + \alpha$ none of the thyristors conduct. During this period $v_0$ attains the value $E$.

Performance of the rectifier such as $V_{OAV}$, $V_{ORMS}$, $I_{OAV}$, $I_{ORMS}$ etc can be found in terms of $\alpha$, $\beta$ and $\theta$. For example
\[
V_{OAV} = \frac{1}{\pi} \int_{\alpha}^{\pi+\alpha} v_0 \, \text{d}t = \frac{1}{\pi} \left[ \int_{\alpha}^{\beta} \sqrt{2} V_i \sin\omega t \, \text{d}t + \int_{\beta}^{\pi+\alpha} \sqrt{2} V_i \sin\theta \, \text{d}t \right] \quad (10.33)
\]

Or
\[
V_{OAV} = \frac{\sqrt{2} V_i}{\pi} \left[ \cos\alpha - \cos\beta + \sin\theta(\pi + \alpha - \beta) \right] \quad (10.34)
\]

\[
I_{OAV} = \frac{V_{OAV} - E}{R} = \frac{V_{OAV} - \sqrt{2} V_i \sin\theta}{Z \cos\phi} \quad (10.35)
\]

Or
\[
I_{OAV} = \frac{\sqrt{2} V_i}{\pi Z \cos\phi} \left[ \cos\alpha - \cos\beta + \sin\theta(\alpha - \beta) \right] \quad (10.36)
\]

It is observed that the performance of the converter is strongly affected by the value of \( \beta \). The value of \( \beta \) in terms of the load parameters (i.e., \( \theta, \phi \) and \( Z \)) and \( \alpha \) can be found as follows.

In the interval \( \alpha \leq \omega t \leq \beta \)
\[
L \frac{di_o}{dt} + Ri_o + E = \sqrt{2} V_i \sin\omega t \quad (10.37)
\]

From which the solution of \( i_o \) can be written as
\[
i_o = \frac{\sqrt{2} V_i}{Z} \left[ \sin(\phi - \alpha)e^{-\frac{(\omega-\alpha)\tau_{amp}}{\tau_{amp}}} - \frac{\sin\theta}{\cos\phi} \left(1 - e^{-\frac{(\omega-\alpha)\tau_{amp}}{\tau_{amp}}} \right) + \sin(\omega t - \phi) \right] \quad (10.38)
\]

Now
\[
i_o \bigg|_{\omega t=\alpha} = 0
\]
\[
i_o \bigg|_{\omega t=\beta} = 0
\]
\[
\therefore \sin(\phi - \alpha)e^{-\frac{(\omega-\alpha)\tau_{amp}}{\tau_{amp}}} - \frac{\sin\theta}{\cos\phi} \left[1 - e^{-\frac{(\omega-\alpha)\tau_{amp}}{\tau_{amp}}} \right] + \sin(\omega t - \phi) = 0 \quad (10.39)
\]

Given \( \phi, \alpha \) and \( \theta \), the value of \( \beta \) can be found by solving equation 10.39.

10.3.3 Inverter Mode of operation

The expression for average dc voltage from a single phase fully controlled converter in continuous conduction mode was
\[
V_0 = \frac{2\sqrt{2}}{\pi} V_i \cos\alpha \quad (10.40)
\]

For \( \alpha < \pi/2, V_d > 0 \). Since the thyristors conducts current only in one direction \( I_0 > 0 \) always. Therefore power flowing to the dc side \( P = V_0I_0 > 0 \) for \( \alpha < \pi/2 \). However for \( \alpha > \pi/2, V_0 < 0 \). Hence \( P < 0 \). This may be interpreted as the load side giving power back to the ac side and the converter in this case operate as a line commutated current source inverter. So it may be tempting to conclude that the same converter circuit may be operated as an inverter by just increasing \( \alpha \) beyond \( \pi/2 \). This might have been true had it been possible to maintain continuous conduction for \( \alpha < \pi/2 \) without making any modification to the converter or load connection. To supply power, the load EMF source can be utilized. However the connection of this source in Fig 10.3 is such that it can only absorb power but can not supply it. In fact, if an attempt is made to supply power to the ac side (by making \( \alpha > \pi/2 \)) the energy stored in the load inductor will be exhausted and the current will become discontinuous as shown in Fig 10.7 (a).
Therefore for sustained inverter mode of operation the connection of E must be reversed as shown in Fig 10.7(b).

Fig 10.8 (a) and (b) below shows the waveforms of the inverter operating in continuous conduction mode and discontinuous conduction mode respectively. Analysis of the converter remains unaltered from the rectifier mode of operation provided $\theta$ is defined as shown.
Exercise 10.3

Fill in the blank(s) with the appropriate word(s)

i) In the discontinuous conduction mode the load current remains _________ for a part of the input cycle.

ii) For the same firing angle the load voltage in the discontinuous conduction mode is _________ compared to the continuous conduction mode of operation.

iii) The load current ripple factor in the continuous conduction mode is _______ compared to the discontinuous conduction mode.
iv) In the inverter mode of operation electrical power flows from the ________ side to the ________ side.

v) In the continuous conduction mode if the firing angle of the converter is increased beyond ________ degrees the converter operates in the ________ mode.

Answers: (i) zero; (ii) higher; (iii) lower; (iv) dc, ac; (v) 90, inverter.

2. A 220 V, 20A, 1500 RPM separately excited dc motor has an armature resistance of 0.75Ω and inductance of 50mH. The motor is supplied from a 230V, 50Hz, single phase supply through a fully controlled bridge converter. Find the no load speed of the motor and the speed of the motor at the boundary between continuous and discontinuous modes when α = 25°.

Answer: At no load the average motor torque and hence the average motor armature current is zero. However, since a converter carries only unidirectional current, zero average armature current implies that the armature current is zero at all time. From Fig 10.6(b) this situation can occur only when θ = π/2, i.e the back emf is equal to the peak of the supply voltage. Therefore,

\[ E_b \bigg|_{\text{no load}} = \sqrt{2} \times 230 \text{ V} = 325.27 \text{ V} \]

Under rated condition \( E_b \bigg|_{1500} = 205 \text{ V} \)

\[ \therefore N_{\text{no load}} = \frac{325.27}{205} \times 1500 = 2380 \text{ RPM} \]

At the boundary between continuous and discontinuous conduction modes from equation 10.32

\[ \sin\theta = \cos\phi \sin(\phi - \alpha) \frac{1 + e^{\pi/\tan\alpha}}{1 - e^{\pi/\tan\alpha}} \]

From the given data \( \phi = 87.27°, \alpha = 25° \)

\[ \therefore \sin\theta = 0.5632 \]

\[ \therefore E_b = \sqrt{2} \text{ V} \sin\theta = 183.18 \text{ Volts} \]

\[ \therefore \text{Motor speed } N = \frac{183.18}{205} \times 1500 = 1340 \text{ RPM}. \]

Summary

- Single phase fully controlled converters are obtained by replacing the diodes of an uncontrolled converter with thyristors.
- In a fully controlled converter the output voltage can be controlled by controlling the firing delay angle (α) of the thyristors.
- Single phase fully controlled half wave converters always operate in the discontinuous conduction mode.
- Half wave controlled converters usually have poorer output voltage form factor compared to uncontrolled converter.
- Single phase fully controlled bridge converters are extensively used for small dc motor drives.
• Depending on the load condition and the firing angle a fully controlled bridge converter can operate either in the continuous conduction mode or in the discontinuous conduction mode.

• In the continuous conduction mode the load voltage depends only on the firing angle and not on load parameters.

• In the discontinuous conduction mode the output voltage decreases with increasing load current. However the output voltage is always greater than that in the continuous conduction mode for the same firing angle.

• The fully controlled bridge converter can operate as an inverter provided (i) $\alpha > \frac{\pi}{2}$, (ii) a dc power source of suitable polarity exists on the load side.

References

